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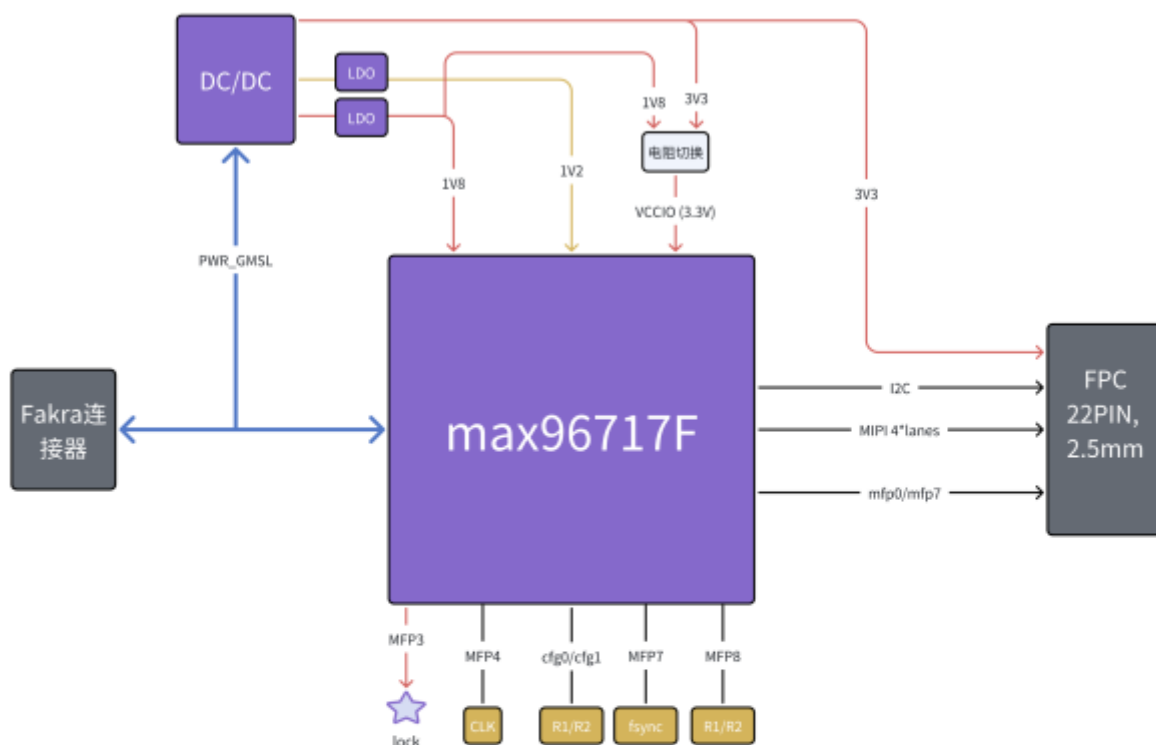
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MAX96717/F serializer (RPI FPC interface)

System Architecture

型号: YYT_SER96717FIO33FPC15



默认配置:

I2C ADDR: 0x80

CLK: XTAL

STP/COAX: coax,

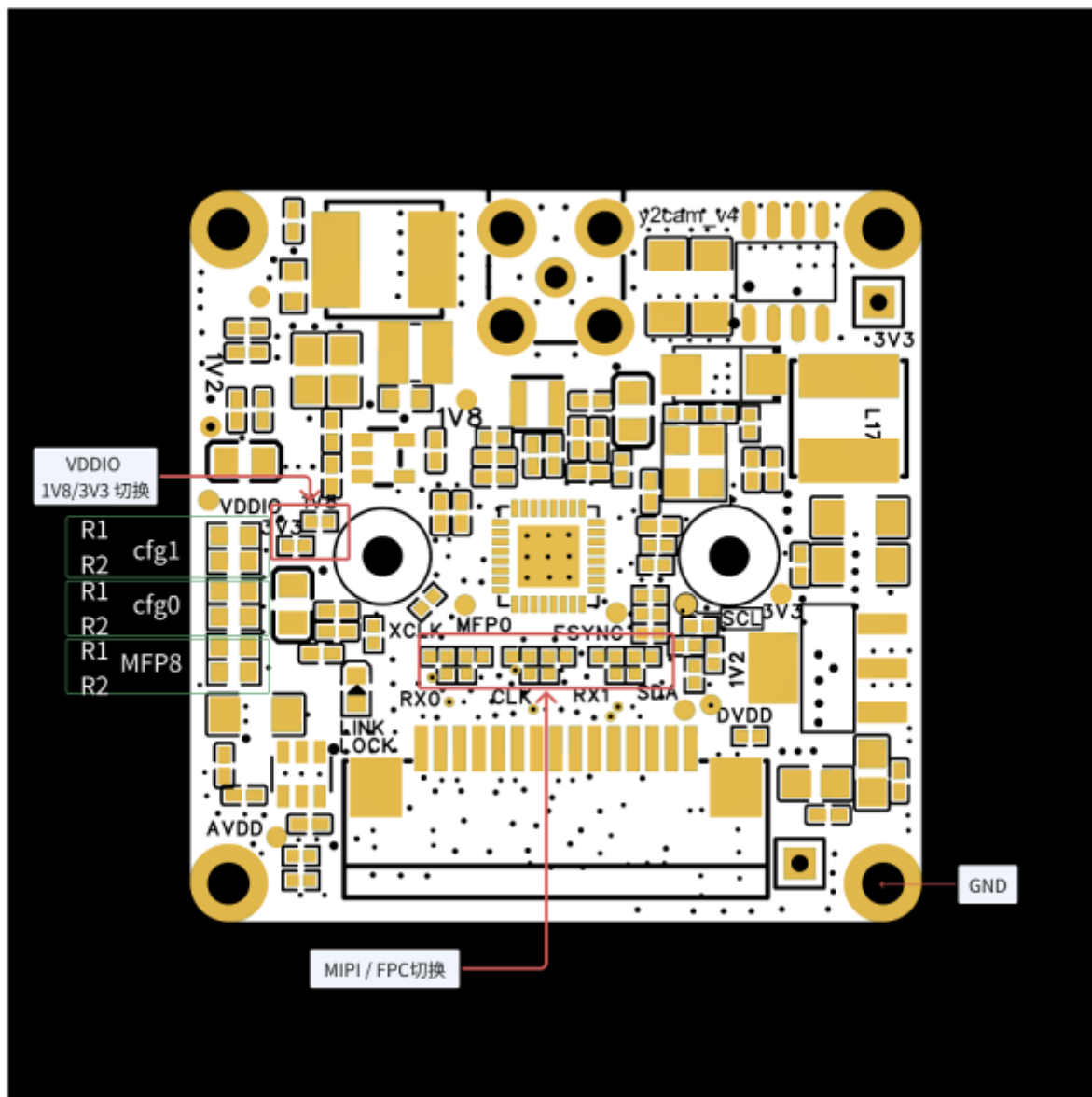
MODE: GMSL2, 3Gbps

VCCIO: 3V3

Main Fetures

- 15pin standard rpi FPC interface. (1mm pitch)
- IO voltage: 3.3V/1.8V switchable with resistor.
- GPIOs are available for use.

Circuit and PCB



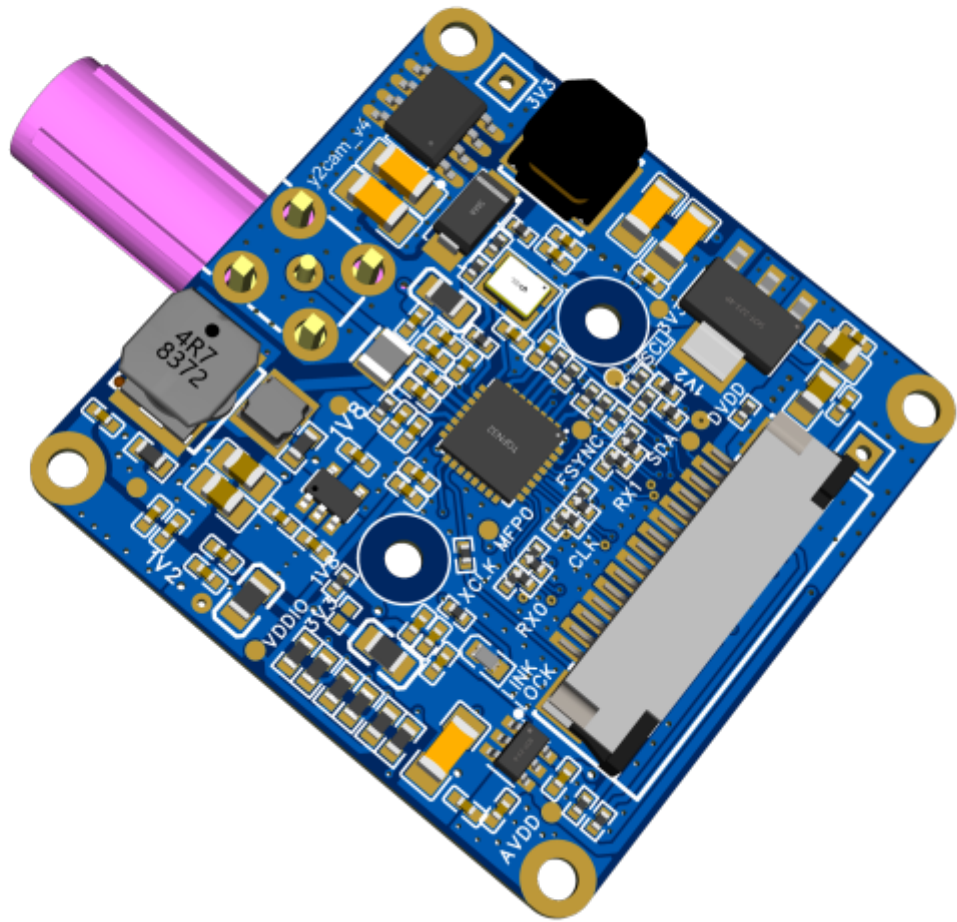
FPC Interface

Raspberry Pi Camera Pinout (15-Pin)		
Pin #	Name	Description
1	GND	Ground
2	CAM_D0_N	MIPI Data Lane 0 Negative
3	CAM_D0_P	MIPI Data Lane 0 Positive
4	GND	Ground
5	CAM_D1_N	MIPI Data Lane 1 Negative
6	CAM_D1_P	MIPI Data Lane 1 Positive
7	GND	Ground
8	CAM_CLK_N	MIPI Clock Lane Negative
9	CAM_CLK_P	MIPI Clock Lane Positive
10	GND	Ground
11	CAM_IO0	Power Enable
12	CAM_IO1	LED Indicator
13	CAM_SCL	I2C SCL
14	CAM_SDA	I2C SDA
15	CAM_3V3	3.3V Power Input

使用说明

1. mipi port connect to de-serializer LANE0/1/CLK.
2. VDDIO is 3.3V
3. Default i2c addr=0x80, default mode=GMSL2/3Gbps/Pixel mode.

3D structure



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