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V1.1 FPC-22PIN rpi-zero interface

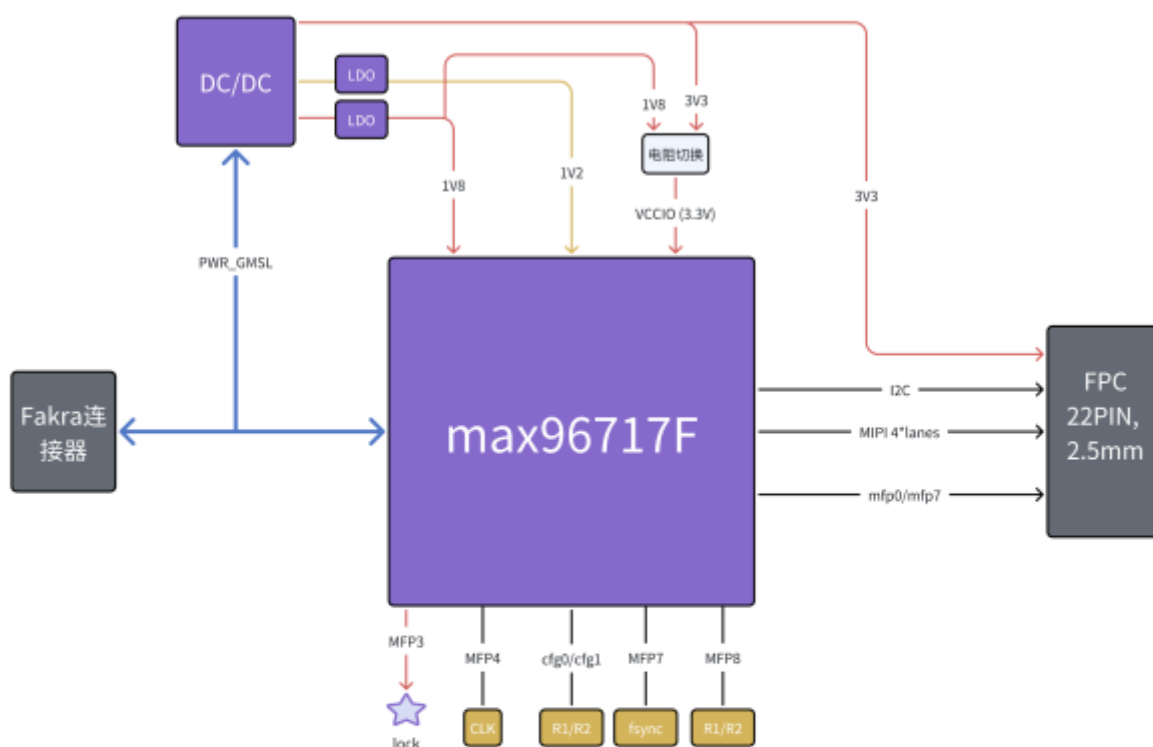
This document also applies to V1.2, V1.3

Main Features

- standard rpi-zero 22pin-0.5mm pitch FPC.
- IO voltage is 3.3V
- GPIOs are available in different forms.

Circuit

型号: YYT_SER96717FIO33FPC15



默认配置:

I2C ADDR: 0x80

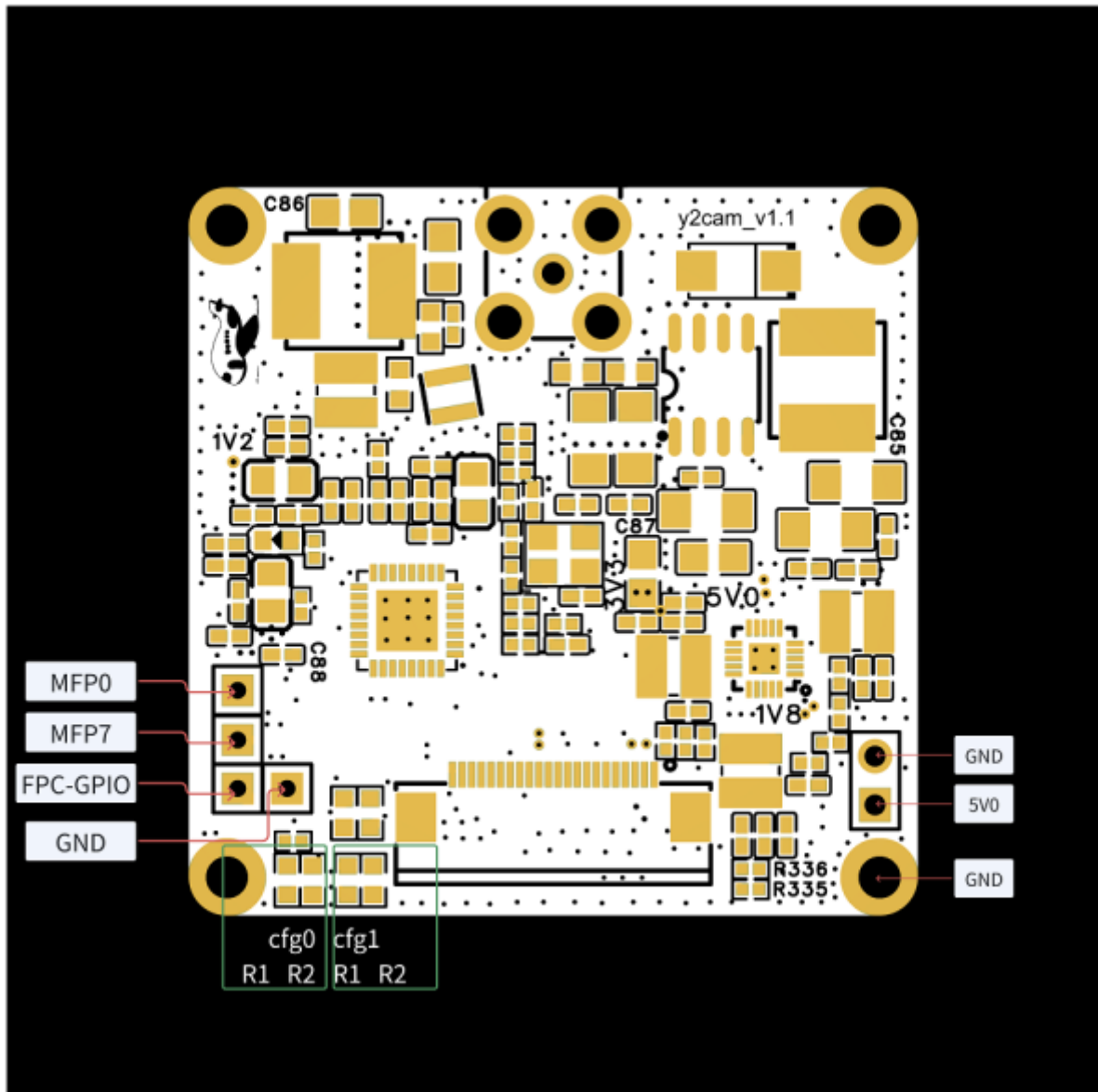
CLK: XTAL

STP/COAX: coax,

MODE: GMSL2, 3Gbps

VCCIO: 3V3

PCB layout



FPC interface

Pin #	Name	Description
1	GND	Ground
2	CAM_D0_N	MIPI Data Lane 0 Negative
3	CAM_D0_P	MIPI Data Lane 0 Positive
4	GND	Ground
5	CAM_D1_N	MIPI Data Lane 1 Negative
6	CAM_D1_P	MIPI Data Lane 1 Positive
7	GND	Ground
8	CAM_CLK_N	MIPI Clock Lane Negative
9	CAM_CLK_P	MIPI Clock Lane Positive
10	GND	Ground
11	CAM_D2_N	MIPI Data Lane 2 Negative
12	CAM_D2_P	MIPI Data Lane 2 Positive
13	GND	Ground
14	CAM_D3_N	MIPI Data Lane 3 Negative
15	CAM_D3_P	MIPI Data Lane 3 Positive
16	GND	Ground
17	CAM_IO0	Power Enable
18	CAM_IO1	LED Indicator
19	GND	Ground
20	SCL	I2C SCL
21	SDA	SCCB serial Interface data IO
22	VCC	3.3V Power Supply

User Guide

1. MIPI port, no lane swap, no PN swap.
2. VDDIO is 3.3V, applies to ALL gpios and I2C.
3. Default i2c addr 0x80, default mode GMSL2/6Gbps.

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